

FIG.1

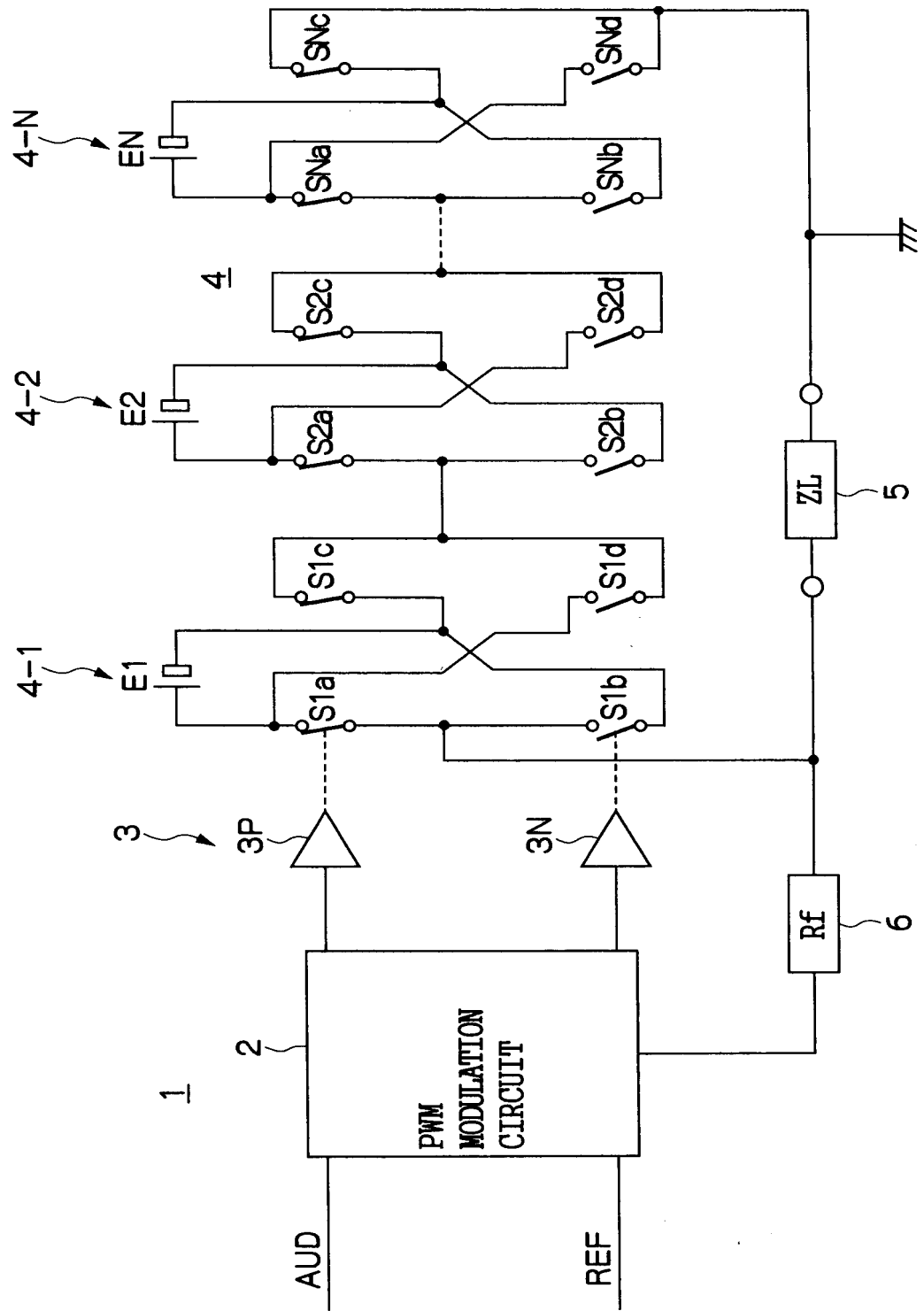


FIG.2

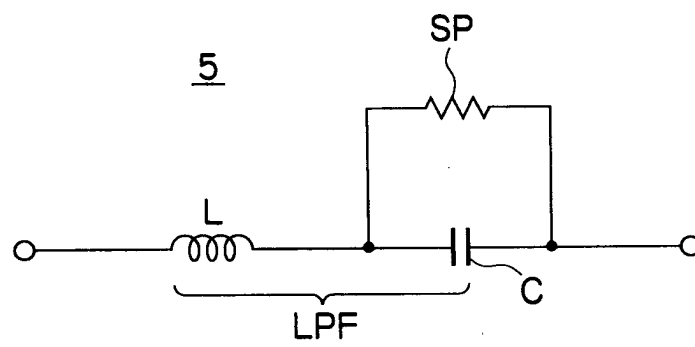


FIG.3

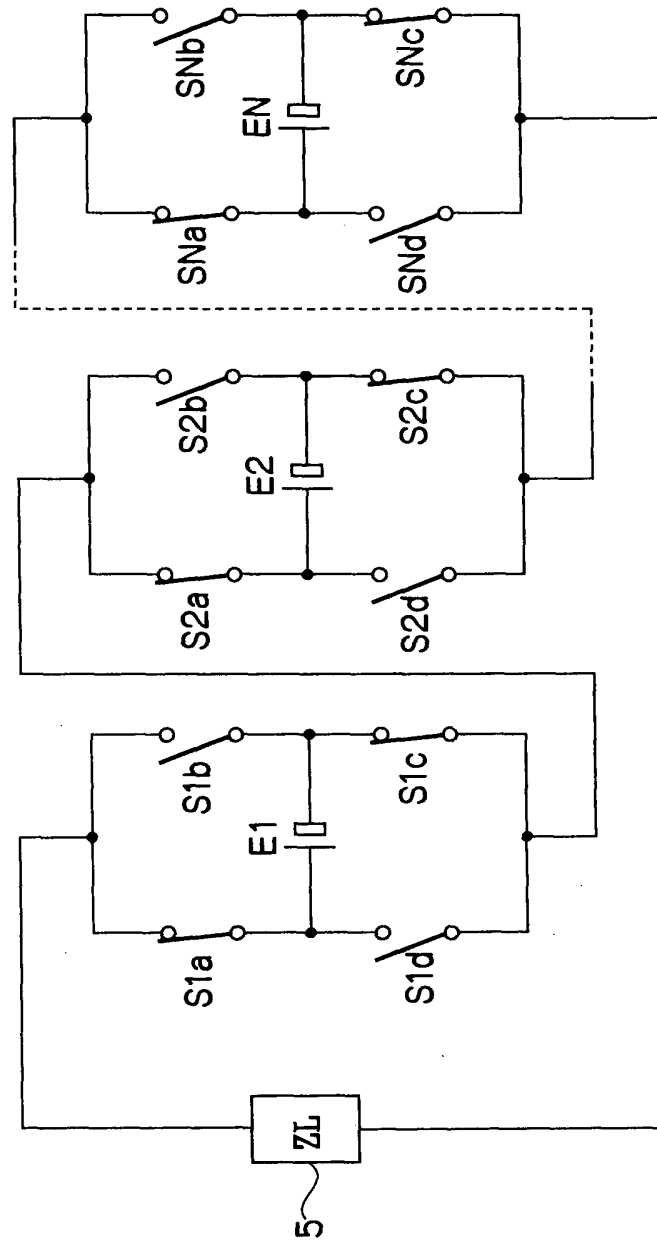


FIG.4

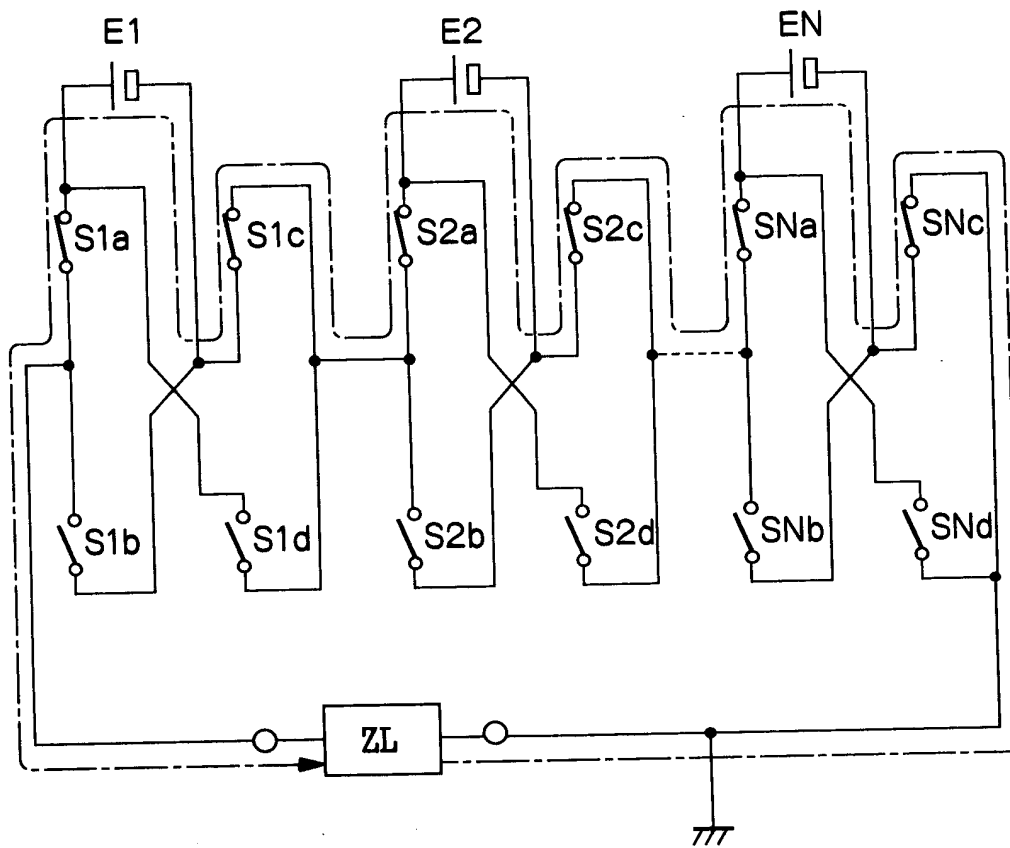


FIG.5

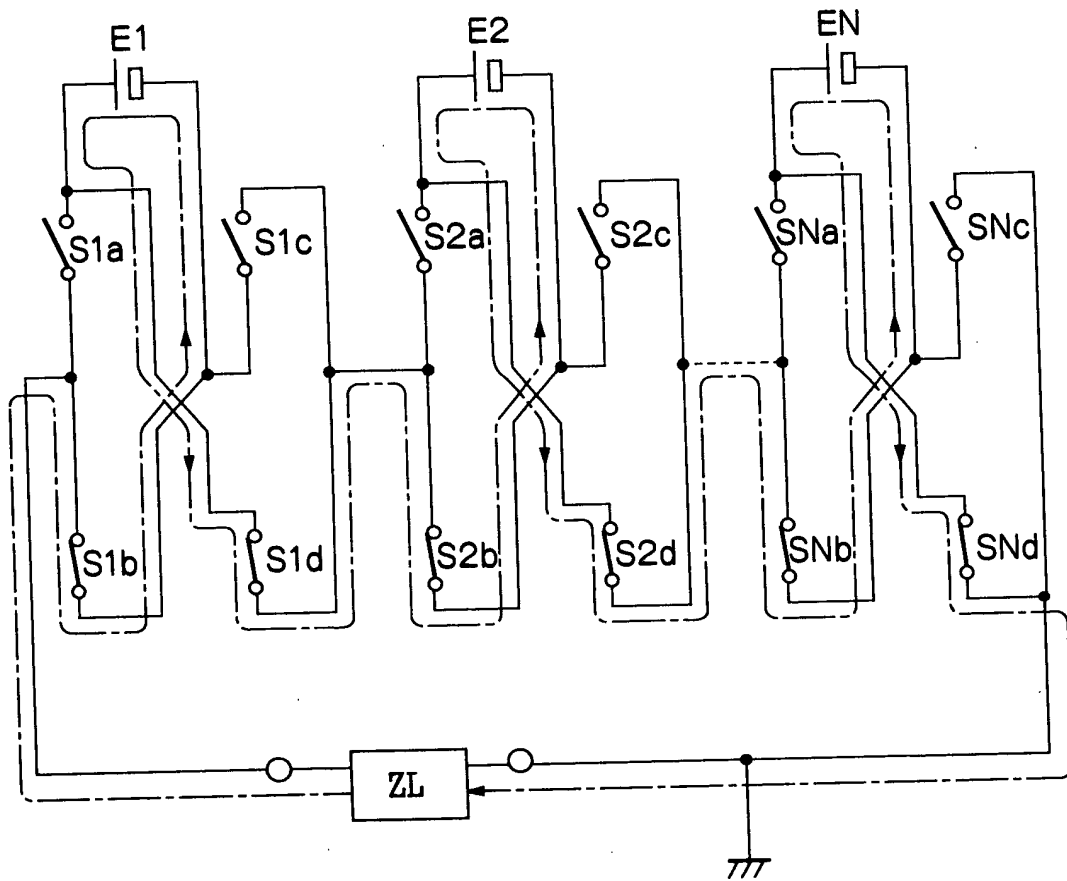


FIG. 6

FIG. 6 is a block diagram of a PWM modulation circuit. The circuit includes an AC source T1, a transformer TR, and a bridge rectifier with diodes D1, D2 and capacitors C1, C2. The rectified output is connected to a series of four full-bridge inverters, each consisting of two MOSFETs (S1a, S1b, S2a, S2b, S1c, S1d, S2c, S2d) and two diodes (SNa, SNb, SNc, SNd). The output of the inverters is connected to a load ZL through a series of four capacitors (C1, C2, C3, C4). The output is also connected to a feedback network consisting of a resistor Rf and a capacitor C5. The feedback network is connected to the input of the modulation circuit 2. The modulation circuit 2 is also connected to an input signal REF and an output signal AUD. The modulation circuit 2 is labeled with a reference numeral 2.

FIG.7

